AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application:

LISTING OF CLAIMS:

1. (*Currently Amended*) A computer ~~Processor~~ program product ~~(1) to be run via a processor system (80)~~ for generating and/or analyzing ~~analysing~~ traffic signals for testing at least a portion of an ~~part of at least one~~ integrated-circuit-environment ~~(8), which integrated circuit environment (8) is designed~~ adapted to handle traffic signals,

~~wherein characterised in that said integrated circuit environment (8) comprises said processor system (80), with~~ said computer ~~processor~~ program product ~~(1)~~ comprising:

at least one generic module; ~~(2,3)~~ and

at least one specific module, wherein said computer program product interfaces with a processor of said integrated-circuit-environment via said at least one generic module, and said ~~with~~ at least one specific module provides a transmission protocol specific to said integrated-circuit-environment to said generic module~~being designed for interfacing said computer program product (1) with a protocol used in said integrated circuit environment (8)~~.

2. (*Currently Amended*) The computer ~~Processor~~ program product ~~(1)~~ according to claim 1, wherein ~~characterised in that~~ said integrated-circuit-environment ~~processor system (80)~~ comprises at least one host processor ~~(80)~~, with generated traffic signals flowing from said host processor ~~(80)~~ to a buffer ~~(87)~~ and from said buffer ~~(87)~~ to at least one further circuit ~~(83,84,85)~~ of said integrated-circuit-environment ~~(8)~~.

3. (*Currently Amended*) The computer ~~Processor~~ program product ~~(1)~~ according to claim 1[[2]], wherein ~~characterised in that~~ generated traffic signals leave said computer ~~processor~~ program product ~~(1)~~ via a software traffic sender ~~(6)~~, with traffic signals to be analyzed ~~analysed~~ arriving at said computer ~~processor~~ program product ~~(1)~~ via a software traffic receiver ~~(7)~~.

4. (*Currently Amended*) The computer ~~Processor~~ program product ~~(1)~~ according to claim 1, wherein ~~characterised in that~~ said protocol comprises a traffic protocol.

5. (*Currently Amended*) The computer ~~Processor~~ program product ~~(1)~~ according to claim 4, wherein ~~characterised in that~~ said traffic protocol comprises an Internet-Protocol or an Asynchronous-Transfer-Mode-Protocol or an Ethernet-protocol.

6. (*Currently Amended*) The computer ~~Processor~~ program product ~~(1)~~ according to claim 1, wherein ~~characterised in that~~ said protocol comprises a bus protocol.

7. (*Currently Amended*) The computer ~~Processor~~ program product ~~(1)~~ according to claim 6, wherein ~~characterised in that~~ said bus protocol comprises a flexbus4 protocol or a SPI4.2 protocol.

8. (*Currently Amended*) A computer ~~Processor system (80)~~ for running a computer ~~processor~~ program product ~~(1)~~ for generating and/or analyzing ~~analysing~~ traffic signals for testing at least a potion of an ~~part of at least one~~ integrated-circuit-environment ~~(8), which integrated circuit environment (8) is designed~~ adapted to handle traffic signals,

wherein ~~characterised in that said integrated circuit environment (8) comprises said processor system (80), with~~ said computer ~~processor~~ program product ~~(1)~~ comprising:

at least one generic module; ~~(2,3)~~ and

at least one specific module, wherein said computer executes said computer program product to interface with a processor of said integrated-circuit-environment via said at least one generic module, and said ~~with~~ at least one specific module provides a transmission protocol specific to said integrated-circuit-environment to said generic module~~being designed for interfacing said computer program product (1) with a protocol used in said integrated circuit environment (8)~~.

9-10. (*Cancelled*).

11. (*New*) The computer according to claim 8, wherein traffic signals generated by said computer program product leave via a software traffic sender, and traffic signals to be analyzed by said computer program product arrive via a software traffic receiver.

12. (*New*) The computer according to claim 8, wherein said protocol comprises a traffic protocol.

13. (*New*) The computer according to claim 12, wherein said traffic protocol comprises an Internet-Protocol or an Asynchronous-Transfer-Mode-Protocol or an Ethernet-protocol.

14. (*New*) The computer according to claim 8, wherein said protocol comprises a bus protocol.

15. (*New*) The computer according to claim 14, wherein said bus protocol comprises a flexbus4 protocol or a SPI4.2 protocol.

16. (*New*) A method for testing at least a portion of an integrated-circuit-environment circuit adapted to handle traffic signals, wherein the method comprises:

generating traffic signals that are sent to said integrated-circuit-environment, wherein first generic module of a computer program product is used to interface with an input of said integrated-circuit-environment, and a first specific module of the computer program product provides a transmission protocol used by said first generic module to interface with said integrated-circuit-environment; and

analyzing traffic signals that are sent to said computer program product from said integrated-circuit-environment, wherein a second generic module of a computer program product is used to interface with the output of said integrated-circuit-environment, and a second specific module of the computer program product provides a transmission protocol used by said second generic module to interface with said integrated-circuit-environment.